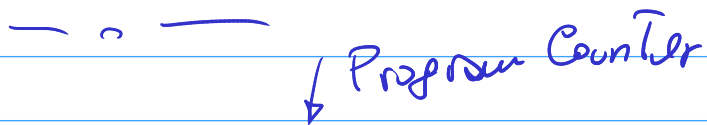


SUBROUTINE

Processor ARM



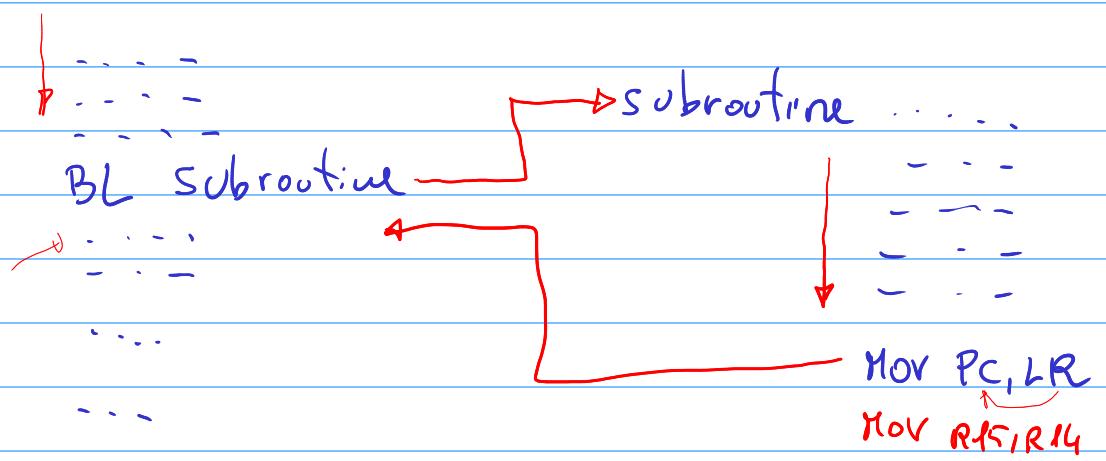
BL target "Branch & Link"



B target ⇒ R15 ← target



BL target ⇒ R14 ← R15 ← target
R15 ← target



STACK (PILA)

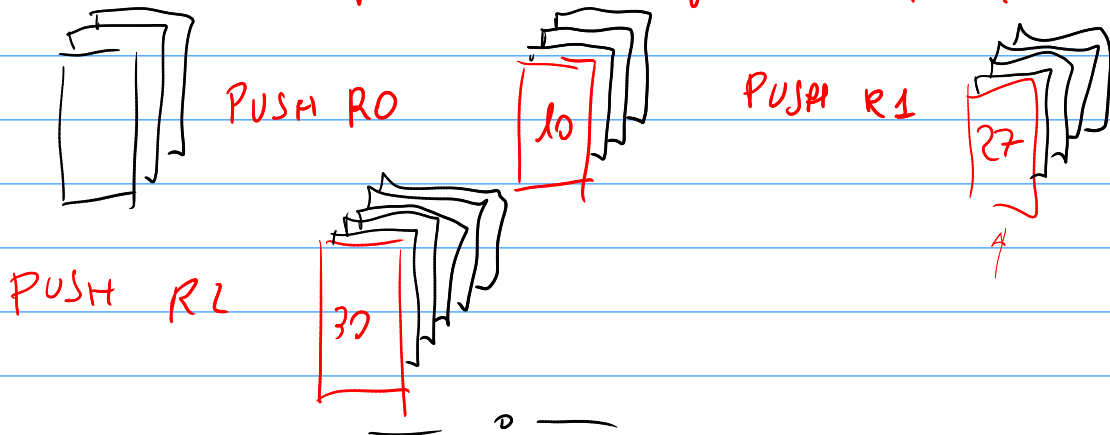
- Una pila di fogli



PUSH 1. passare un nuovo foglio

POP 2. prendere il primo foglio della pila

Conservare e riprendere i registri R0, R1, R2



RECUPERARE I REGISTRI

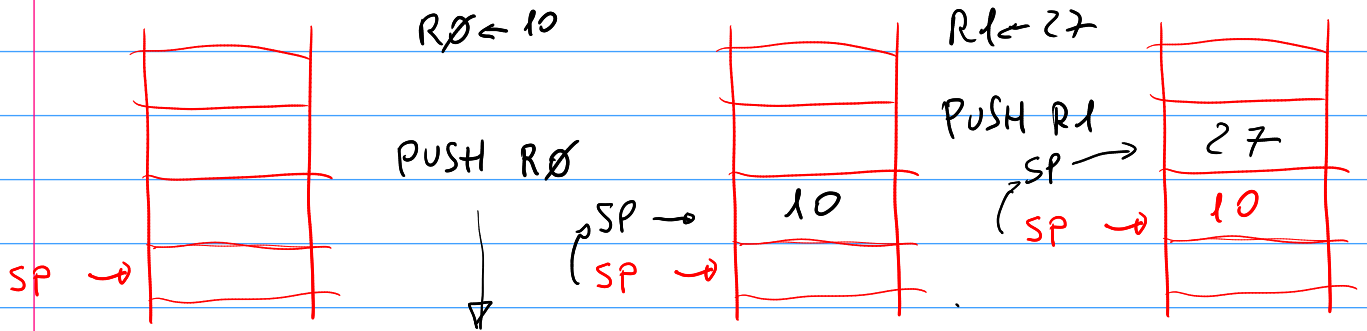
POP R2 ← 30 ; POP R1 ← 27 ; POP R0 ← 10

STACK → LIFO Last-In-First-Out

STACK DI UN PROCESSORE

SP = STACK POINTER

Contiene l'indirizzo della "stack" in memoria

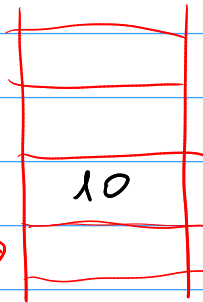


$R0 \leftarrow 10$

$PUSH R0$

SP \rightarrow

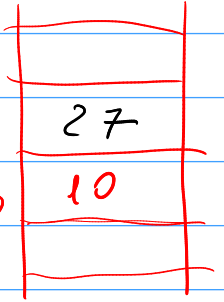
SP \rightarrow
SP \rightarrow



$R1 \leftarrow 27$

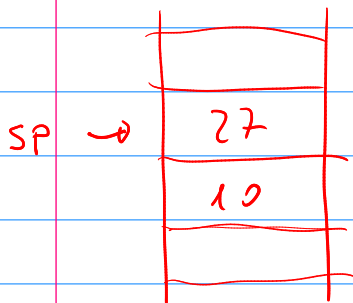
$PUSH R1$

SP \rightarrow
SP \rightarrow



$SP \leftarrow SP - 4$

$[SP] \leftarrow R0$

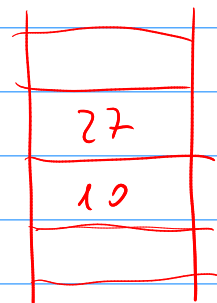


$POP R1$

$R1 \leftarrow [SP] (27)$

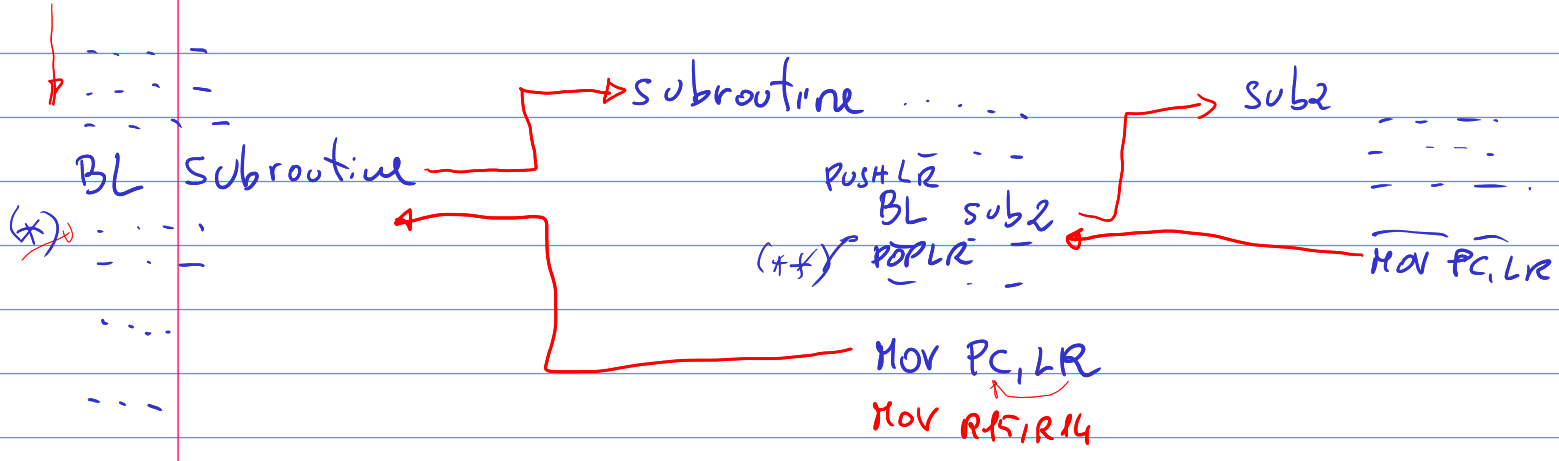
$SP \leftarrow SP + 4$

SP \rightarrow
SP \rightarrow



BL e CALL

main



main

