Benchmarking of Computer Vision Algorithms for Driver Monitoring on Automotive-grade Devices

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Abstract—The continuing evolution of technologies in the automotive industry has led to the development of the so-called Advanced Driver Assistance Systems (ADAS). ADAS is the term used to describe vehicle-based intelligent safety systems designed to support the driver, with the aim to significantly improve his safety, and the driving safety in general. In terms of development, current ADAS technologies are based on control functions about the vehicle movements with respect to the objects and entities detected in the same environment (e.g., other vehicles, pedestrian, roads, etc.). However, there is an ever growing interest on the use of internal cameras to infer additional information regarding the driver status (e.g., weakness, level of attention). The purpose of such technologies is to provide accurate details about the environment in order to increase safety and smart driving. In the last few years, Computer Vision technology has achieved impressive results on several tasks related to recognition and detection of customized objects/entities on images and videos. However, automotive-grade devices’ hardware resources are limited, with respect to the once usually required for the implementation of modern Computer Vision algorithms. In this work, we present a benchmarking evaluation of a standard Computer Vision algorithm for the driver behaviour monitoring through face detection and analysis, comparing the performances obtained on a common laptop with the same experiments on an existing commercial automotive-grade device based on the Accordo5 processor by STMicroelectronics.

Index Terms—Advanced Driver Assistance Systems (ADAS), Computer Vision, STM Accordo5

I. INTRODUCTION

In recent years, the automotive manufacturers focused on developing advanced assisting technologies to improve driving comfort and safety for the driver. This increasing interest has led to the development of the Advanced Driver Assistance Systems (ADAS). The term ADAS refers to automated control functions and technologies designed to detect and evaluate the environment, in order to alert the driver and take control of the vehicle. Specifically, one of the main ADAS functions consist of processing information about the environment outside of the vehicle, (e.g., road infrastructures, road traffic, etc.), in order to provide effective support while driving by using a wide range of sensors [1]. With the significant progress of ADAS technologies, there is a continuing demand to develop effective warning systems not only to detect the presence of obstacles from the exterior environment, but also to deliver early detection of a car driver’s inadequate physiological condition. The main objective is to minimize the risk of road accidents. Indeed, recent studies established that a considerable percentage of crashes are related to the bad driver status [2]. As a consequence, the most recent solutions involve the use of advanced Computer Vision algorithms for the analysis of head pose estimation, eye blink detection alongside the analysis of physiological signals such as PhotoplethysmoGraphy (PPG) and Electrocardiography (ECG) [3]–[8]. In this context, Computer Vision technologies have a crucial role for the vehicle industry because they allow algorithms to automatically analyse driver’s face, gaze, and emotions to detect inattentiveness, drowsiness, road rage, and other potential dangerous situations [9], [10]. Computer Vision has achieved high levels of precision and accuracy on interpreting important visual information contained in images and video data. However, the high accuracy comes at the price of large computational cost. As a result, dedicated hardware devices, from the application of specific processors, are needed to optimize complex workloads of the Computer Vision methods. In automotive industry, the current capabilities of automotive-grade hardware devices are limited. For this reason, the scientific community is working intensely to overcome the constraints in intelligent in-vehicle technologies. Our study describes the development, implementation and testing of a drowsiness detection system that tailors to the car environment. Specifically, the main objective is to compare the performances of a common laptop and an existing commercial automotive-grade device in order to provide a benchmarking evaluation of a Computer Vision algorithm for driver monitoring through eye blink detection. The hardware used to run the experiments.
consists of a ST board based on Accordo 5 processor, developed by STMicroelectronics [11]. Although the results are not encouraging, since the board takes a long time to process the algorithms compared to a common laptop, we aim to open several paths for further studies to speeding up Computer Vision methods and implementing more advanced solutions on this automotive-grade technology. This paper is organized as follows. Previous related works exposing Computer Vision techniques that have been employed for automatic drowsiness detection are presented in Section II. The description of Computer Vision algorithms used for face and eye blink detection are presented in Section III. Section IV describes our experimental setup, providing details about the dataset and the hardware devices used to carry out our study. Section V shows the quantitative results of our benchmarking evaluation. The final considerations and a brief discussion on possible future research are presented in Section VI.

II. RELATED WORKS

Several studies have investigated the application of Computer Vision algorithms with the purpose of estimating driver’s status. In [12], the authors propose a system that measures eye blinking rate and eye closure duration. The main contribution of this work is the skin-segmentation procedure. Specifically, the authors implemented a robust and adaptive skin segmentation method to detect facial regions (e.g., eyes, lips, nose, eyebrows, etc.) by extracting Speeded Up Robust Features (SURF) facial features [13]. After performing segmentation, eye blinking frequency and eye closure are estimated by monitoring the positions of irises through time. However, the calculation of Euclidean distance between SURF descriptors and the skin segmentation take a long time to process. Indeed, the work set the computation time as the critical point, which is not feasible for real-time applications. In this context, developing a robust, reliable, and efficient embedded system has become crucial in the automotive industry. The technological advances and expansion of vehicle technology has led to the development of more sophisticated embedded systems for the vehicle control. Embedded systems are commonly used in several applications including infotainment and telematics, safety and powertrain control. The recent electronic innovation has contributed to introduce the development of new features for different functionalities. Hence, the modern days embedded systems include microprocessors capable of efficiently running Computer Vision algorithms for several applications. However, there are many concerns related to automotive-grade hardware resources which present some limitations in terms of size, memory, power, cost, etc. Based on this, we listed some interesting solutions which balance all requirements and limitations in order to execute advanced algorithm for several ADAS applications. In [14], a novel approach for the real-time detection of car driver drowsiness and alcoholic intoxication is proposed. The authors used an embedded system board Raspberry-pi with Raspbian-OS, including Python-IDLE and OpenCV library. The overall system is composed of an alcohol gas sensor application and an open source embedded system board Arduino Uno with I2C protocol for performing the alarm notification. To capture the car driver’s face and eyes, they used an open source 5-megapixel digital camera to record a video while driving. Another example is [15], it uses a 5-megapixel Raspberry camera to capture driver’s face and eyes. To detect the blink eye duration of the driver, the authors calculate Eye Aspect Ratio (EAR) by using Haar Cascade Classifiers. The Raspberry Pi 3 with Raspbian Operating Systems is used to perform the entire algorithm for drowsiness detection. When the driver closing his eyes for more than a fixed threshold, a warning message is generated. This alert message is sent to the car owner along the car plate number through the use of a Ubidots cloud service and Twilio API.

III. DROWSINESS DETECTION SYSTEM

This section presents the pipeline developed to evaluate car driver’s drowsiness. Our approach is based on assessing driver’s fatigue level through facial analysis by extracting face component features (e.g., eyes, nose, mouth, etc.) from human face image. Feature extraction is a key component of most computer vision applications, from face recognition to age estimation [16], since providing a vast amount of information. In automotive field, facial analysis systems have been widely used to measure the eye closure duration in order to find hints of drowsiness. Indeed, previous research activities have demonstrated the existing correlation between vigilance and eye blink duration [17]. Inspired by the work of Soukupová and Čech [18], we computed the Eye Aspect Ratio (EAR) values, estimating the eye blinks, with the aim of assessing the car driver’s drowsiness. Fig. 1 shows the overall pipeline. At the first stage, we collected video sequences of car driver’s face emulating a drowsy scenario. To conduct our experiments, we used a common laptop and a development board based on Accordo5 processor in order to compare their

![Pipeline to detect car driver’s drowsiness.](image-url)
In this stage, we created an instance of Facemark regressing local binary features is a very cheap operation. At approach is the lower cost in terms of computation time since landmarks independently. The main advantage of the proposed perform regression for the final output and elaborating facial is related to learning discriminative local binary features to described in [20] consists in a set of local binary features, and a locally principle for learning those features. The main novelty is related to learning discriminative local binary features to perform regression for the final output and elaborating facial landmarks independently. The main advantage of the proposed approach is the lower cost in terms of computation time since regressing local binary features is a very cheap operation. At this stage, we created an instance of Facemark class which is wrapped inside OpenCV pointer, optimizing the memory management. We load model related to landmarks detector which has been trained on a considerable number of training images and the corresponding annotations. We repeat this procedure for each video frames of recorded sequences. We also estimate computing time in order to measure the performance of both devices. Although the dlib library 1 is considered the "gold standard" to estimate facial landmarks in Computer Vision applications, it requires a high computational cost. For this reason, we adopted the Facemark API. Once detecting the 68-facial landmarks, we developed an effective drowsiness detection system by taking into account the eye landmarks. According to [18], Eye Aspect Ratio (EAR) value represents the distance used to determine if a person is blinking. Each eye is represented by 6 $(x, y)$-coordinates. Also, we choose the number of consecutive frames in which a driver closes his eyes. This is a threshold to not exceed. When the driver close his eyes and the EAR value is below a given threshold for a number of consecutive frames, a warning message appears to alert that the fatigue of car driver is high and could be dangerous. The EAR value is computed using the following equation:

$$\text{EAR} = \frac{||p2 - p6|| + ||p3 - p5||}{2 ||p1 - p4||}$$

where $p1$,...,$p6$ represent the 2D eye landmarks locations. The numerator indicates the Euclidean distance between vertical

![Fig. 2. Example of blink eye. The Eye Aspect Ration (EAR) is computed by using the distances of eye landmarks.](image)

IV. RESOURCES AND MATERIALS

A. Dataset

The dataset is composed of 12 videos. Specifically, this set contains video sequences of a face of people while driving. We acquired video sequences from drivers of different genders and ages. For the sake of completeness, we set up experiments also including drivers while wearing eyeglasses, in order to evaluate the effectiveness of the drowsiness detection algorithm considering different scenarios. The facial camera were at a distance of approximately 30 cm from the subject. In particular, it was placed in front of the driver in order to record the subject’s frontal face while driving. Each video is 100 seconds in length. The resolution is set to 720p and frame rate is 30 fps. We emulate a drowsy scenario by performing eye blink closures while recording videos under a high light condition.

B. Hardware and devices

In this section, we briefly provide an overview of the used hardware, listing the main characteristics of each device. In order to evaluate the benchmarks, we used OpenCV (v. 3.4.3) installed on both systems, running C++ (std11) implementation of drowsiness detection algorithm to improve overall computational efficiency.

**LAPTOP.** Experiments were carried out on a laptop with an Intel Core i7 4710HQ CPU with 4 cores, 16GB of RAM and a N550JK motherboard, running Ubuntu 18.04 LTS.

**ST BOARD.** The other device involved in experiments is the development board STA1295 based on Accordo 5 processor produced by STMicroelectronics (software environment with embedded Linux) [11]. Specifically, the Accordo5 Evaluation Board (A5EVB) is a highly integrated “Car Radio,” mounting

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1 Dlib library: [http://dlib.net/](http://dlib.net/)
the STA1295 version of the Accordo5 device, which is packaged in a 19x19mm LFBGA Package with 529 balls, pitch 0.8mm. The board is also composed of a 720p 10-inch Display Panel.

**ACCORDO 5.** The Accordo 5 multi-processor is a new line of digital-infotainment chips developed by STMicroelectronics [11]. The main advantage of the Accordo 5 chips is their integration of a dedicated, isolated ARM® Cortex®M3 core that secures the interface between the head unit and the main vehicle network. The microcontroller features built-in boot-code authentication, secure interconnect, and high-performance data encryption to manage secure CAN (Control Area Network) connectivity in real time. Furthermore, Accordo 5 devices provide several attractive features, such as smartphone mirroring (e.g., music and navigation services, etc.). In addition, the Accordo 5 family includes many state-of-art features, such as digital instrument clusters, consisting of complex and elegant displays which replace conventional dials and indicator lamps. Moreover, these chips integrate graphics, video and audio functionality alongside Audio/Video/Navigation (AVN) head units and dual screen capability in order to allow simultaneous user-interface plus rear-view camera with navigation and video previews, making driver safer and more comfortable.

**YOCTO.** The Operating System on STA1295 Accordo5 embedded automotive platform is ad-hoc YOCTO Linux-based distribution (v. 1.8) [21]. Specifically, YOCTO is a collection of tools and meta-data that allows a developer to build their own custom distribution of Linux for their embedded platform. The main benefit of Yocto consists in building custom Linux OS for about any kind of computing device, allowing to create Linux images for all the major hardware architectures. In addition, it makes the creation of custom Linux distribution faster, easier and cheaper, which is a key aspect when developing for embedded products. The main parts of the Yocto Project consist of the build system, the package meta-data, and the developer tools. Specifically, the build system uses a tool called `bitbake` to process the meta-data and produce a complete Linux distribution. It also builds the kernel, libraries, and programs that comprise a Linux distribution. In order to perform the deployment to the target device, it prepares the resulting software by placing it into appropriate bundles (including packages, images, or both). Moreover, Yocto provides application development and debugging to support developers. Fig. 3 overviews the Yocto workflow.

**V. RESULTS AND DISCUSSION**

In this section, we present and analyze the obtained results. In order to perform the proposed benchmark evaluation, we executed the above described drowsiness detection algorithm 3 times, estimating the average time taken by both used devices to run it. Table I shows the computational time for each video of the dataset. The average time is highlighted in bold. As evident, the results revealed that the performance of the common PC is quite efficient. More specifically, we observed that the maximum running time required to compute drowsiness detection algorithms is \(31,515\) seconds in relation to the video in which we performed the highest number of eye blink closures (Video 6), while the minimum running time is about 24 seconds. On the other hand, our experiments reported poor performances concerning the embedded STA1295 board. As expected, our tests show that the automotive designed board yields longer computational time than the common laptop. In general, to run a Computer Vision algorithm on our embedded system faced with several limitations both in terms of memory and power consumption, since it was not developed to execute demanding algorithms. In the process of analyzing performances of the used board, it turned out that the most
TABLE I

THE PROCESSING TIME OF A COMMON LAPTOP.

<table>
<thead>
<tr>
<th>Run</th>
<th>Video 1</th>
<th>Video 2</th>
<th>Video 3</th>
<th>Video 4</th>
<th>Video 5</th>
<th>Video 6</th>
<th>Video 7</th>
<th>Video 8</th>
<th>Video 9</th>
<th>Video 10</th>
<th>Video 11</th>
<th>Video 12</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>24.606 s</td>
<td>25.469 s</td>
<td>26.689 s</td>
<td>24.162 s</td>
<td>25.941 s</td>
<td>31.831 s</td>
<td>27.336 s</td>
<td>28.641 s</td>
<td>27.091 s</td>
<td>27.042 s</td>
<td>27.127 s</td>
<td>28.132 s</td>
</tr>
<tr>
<td>2</td>
<td>24.380 s</td>
<td>24.786 s</td>
<td>25.501 s</td>
<td>23.376 s</td>
<td>25.961 s</td>
<td>31.118 s</td>
<td>27.812 s</td>
<td>28.429 s</td>
<td>27.269 s</td>
<td>27.605 s</td>
<td>27.306 s</td>
<td>28.218 s</td>
</tr>
<tr>
<td>3</td>
<td>25.138 s</td>
<td>25.797 s</td>
<td>25.532 s</td>
<td>23.263 s</td>
<td>26.127 s</td>
<td>31.597 s</td>
<td>27.671 s</td>
<td>28.571 s</td>
<td>27.656 s</td>
<td>26.911 s</td>
<td>27.467 s</td>
<td>28.167 s</td>
</tr>
</tbody>
</table>

Avg Time 24.708 s 25.351 s 25.907 s 23.600 s 26.010 s 31.515 s 27.606 s 28.547 s 27.139 s 27.186 s 27.300 s 28.172 s

TABLE II

THE PROCESSING TIME OF STA1295 ACCORDO5 EMBEDDED AUTOMOTIVE PLATFORM.

<table>
<thead>
<tr>
<th>Run</th>
<th>Video 1</th>
<th>Video 2</th>
<th>Video 3</th>
<th>Video 4</th>
<th>Video 5</th>
<th>Video 6</th>
<th>Video 7</th>
<th>Video 8</th>
<th>Video 9</th>
<th>Video 10</th>
<th>Video 11</th>
<th>Video 12</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>551.671 s</td>
<td>682.129 s</td>
<td>704.963 s</td>
<td>682.965 s</td>
<td>543.469 s</td>
<td>613.612 s</td>
<td>536.201 s</td>
<td>650.899 s</td>
<td>651.623 s</td>
<td>633.570 s</td>
<td>683.854 s</td>
<td>663.421 s</td>
</tr>
<tr>
<td>2</td>
<td>525.358 s</td>
<td>648.010 s</td>
<td>671.192 s</td>
<td>685.960 s</td>
<td>715.312 s</td>
<td>583.900 s</td>
<td>534.827 s</td>
<td>633.245 s</td>
<td>504.850 s</td>
<td>616.559 s</td>
<td>690.002 s</td>
<td>729.140 s</td>
</tr>
<tr>
<td>3</td>
<td>540.734 s</td>
<td>653.270 s</td>
<td>676.755 s</td>
<td>679.827 s</td>
<td>688.844 s</td>
<td>597.820 s</td>
<td>535.847 s</td>
<td>540.152 s</td>
<td>609.027 s</td>
<td>671.887 s</td>
<td>517.740 s</td>
<td>635.540 s</td>
</tr>
</tbody>
</table>

Avg Time 539.254 s 661.136 s 684.304 s 682.935 s 649.208 s 601.777 s 535.625 s 608.099 s 588.500 s 640.605 s 630.532 s 676.034 s

demanding part was related to I/O operations. To minimize the computational costs, we optimized I/O operations to increase the processing efficiency. In this respect, we performed the following steps:

- video decoding, computing CV algorithms, frame pushing on a buffer;
- closing reading stream
- frame popping from buffer and writing on file system

Despite attempts to increase the overall performance, the automotive-grade board takes long time to compute the implemented drowsiness detection algorithm. As seen in Table II, the results reported an average running time over 530 seconds for each video which is not acceptable for a real-time application. Indeed, the resulting performance is a consequence of multiple factors, spanning over OpenCV implementation and video properties (resolution, fps, etc.). Although it is certainly possible to cross-compile the OpenCV source code on embedded devices, memory constraints and other architectural considerations may pose a problem, complicating the task of optimizing CV functions on a new target device. Furthermore, video properties, such as resolution, fps, etc., will directly affect the efficiency of the overall system. As previously reported, we reduced the resolution of videos (Standard VGA resolution) since our automotive-grade device presents limited resources. To sum up, we used a ready-to-go development board taking advantage of its integrated features to execute a Computer Vision algorithm for evaluating drowsiness of a car driver. Despite its limitations, and consequently the poor results reported in Table II, our findings suggest that this device could be employed for automotive applications based on running CV algorithms. In fact, by developing a fierce trade-off with respect to memory usage, I/O bandwidth and algorithm complexity, we will provide a significant improvement to maximize the potential of Computer Vision functions on our embedded platform.

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VI. Conclusions

In this paper, we proposed a benchmarking evaluation of Computer Vision algorithms, which allowed us to perform direct comparisons between the performances of an automotive designed computational board (STA1295 Accordo5 MCUs) with a common laptop, with the aim to assess the possibility to run Computer Vision algorithms on already existing hardware designed for automotive purposes. Although the results are not satisfactory, we observed that it is possible to run Computer Vision algorithms on our embedded development board. In this instance, we remain confident that, with adequate improvements, it is possible to implement Computer Vision algorithms on our automotive-grade device in order to define an efficient attention monitoring system. Finally, there are a few directions for further studies. Specifically, we aim to implement more advanced Computer Vision algorithms to provide a complete study regarding driver status analysis. Moreover, considering the remarkable success of Deep Learning architecture over the last years [22], our future works will focus on optimizing the use of Deep Learning approaches on our automotive board based on Accordo 5 processor, alongside reducing the computational cost for the development of a useful tool for the automotive industry.

REFERENCES


