

**Corrections to the textbook by Patrick R. Schaumont**  
**“A Practical Introduction to Hardware/Software Codesign”**  
**Second Edition, Springer (2012)**

produced in the framework of the course on Dedicated systems for the graduate program in Computer Science, 2014–2019  
teacher: G. Scollo University of Catania, Department of Mathematics and Computer Science.

*Legenda:*

S = Section, A = Appendix, T = Table, F = Figure, L = Listing, E = Equation, P = Problem,

p = page, n = footnote, l = line, *<c>* = caption, *<d>* = delete, *<i>* = insert

S A T F L E P	p(n)	l	Errata	Corrigé
F 1.2	7	14	2.	2
S 1.1.3	9	-4	i801system	i8051system
		-2	28–38	27–37
	11	2	40–46	39–45
S 1.6	23	18	model	level
S 2.2	41	22	two times	one time
E (2.2)		24	[2]	[1]
E (2.3)		29	[2 – 4 0] [2]	[2 – 4 0] [1]
S 2.2.2	43	5	16.128	16 · 128
S 2.3.1	46	13	takes jeopardizes	jeopardizes
S 2.5	51	1	<b>Pipelining</b>	<b>Pipelining</b>
S 2.5.1	51	2	to convert	convert
S 2.5.4	55	12	decreases	increases
S 2.7	56	4	build	built
L 3.1	64	14	assert(fifo_size(F) <= 10);	<i>&lt;d&gt;</i>
		30	MAXFIFO = (F->rptr - F->wptr) + 1;	MAXFIFO = (F->rptr - F->wptra);
S 3.1.2	66	8	round-robing	round-robin
F 3.6(b)	68	3	t[0]	t[1]
		7	= c	= a
S 3.1.3.1	71	2	can	cannot
	74	2	{ {&q1}, {&q2} }	{ {&q1}, {&q2} }
L 3.4	76	4	, fft2_0_out2, fft2_0_out3	<i>&lt;d&gt;</i>
		5	, fft2_1_out2, fft2_1_out3	<i>&lt;d&gt;</i>
S 3.1.4	76	6	series	series of
S 3.2.2	80	3	x0.c2 + x1.c1 + x2.x0	x0 · c2 + x1 · c1 + x2 · c0
L 3.5	83	21		<i>&lt;d&gt;</i>
S 3.3	85	12	dfsyst.c	df.c
S 3.4	86	15	and –	and
S 4.2	92	-5	v1;	v1, v2;
S 4.6	103	14	then	than
		24	its' name refers says	its name says
S 4.8	106	1	from	form
P 4.4	108	3	the these	these
F 5.6	125			<i>&lt;see next page&gt;</i>
T 5.4	127	<i>&lt;c&gt;</i>	5.9	5.13
S 5.4.1	128	1	5.14	5.7
S 5.4.2	129	1	upon and	upon an
		5	send	sends
L 5.16	131	17	a + 1 : a - 1	a - 1 : a + 1
L 5.19	135	3	, s5	<i>&lt;d&gt;</i>
L 5.20	136	3	z4	z4 : ns(3)
		5	, s5	<i>&lt;d&gt;</i>
L 5.21	140	60		<i>&lt;d&gt;</i>
T 6.1	166	10	ACC — S-Bus	ACC   S-Bus
L 6.2	170	29	ACC	SBUS
		30	SBUS - 1	ACC + 1
		54	IR	ACC
	173	196	(ns(WLEN)) (shft_in[0] #	shft_in[0] # ((ns(WLEN-1))
		200	shft_out	shft_in
	174	219		<i>&lt;d&gt;</i>
L 7.1	195	13	(max > m)	(m > max)
L 7.2	196	19	movge	movlt
S 7.1.2	198	3	for ...while	while ...for
T 7.2	209	5	class	close

*cont.*

S A T F L E P	p(n)	1	Errata	Corrige
T 7.2	209	5	class	close
S 7.3.3	210	5	objdump	gcc
S 7.4.1	215	16	0xf4	0x13c
		18	228	300
S 7.6.3	229	18	implies the	imply
F 7.9	212			<see below>
E (8.1)	241	-1	$\frac{291 \cdot 10^6}{28 \cdot 32} \cdot 2.1$	$\frac{291 \cdot 10^6}{28 \cdot 32} \cdot 2.1$
S 8.4.2	255	7	input	output
		8	output	input
L 8.3	256	31	rreq, rack, rid	rreq
L 8.4	257	<c>	ping-point	ping-pong
		7	xdata	—xdata
S 8.4.3	258	-6	line 10	line 11
		-5	line 11	line 12
L 8.6	260	20-21		<d>

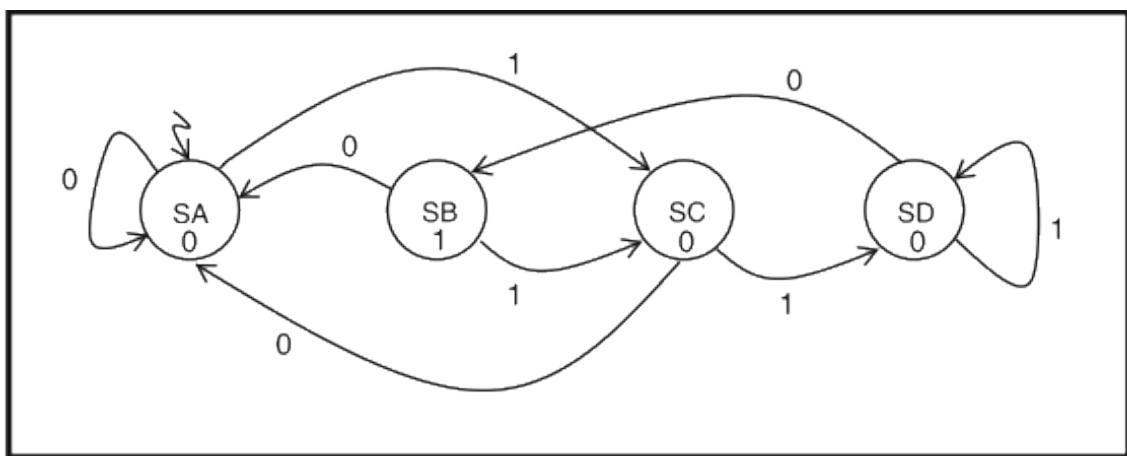


Fig. 5.6 diagram corrige

#### During Execution of Accumulate

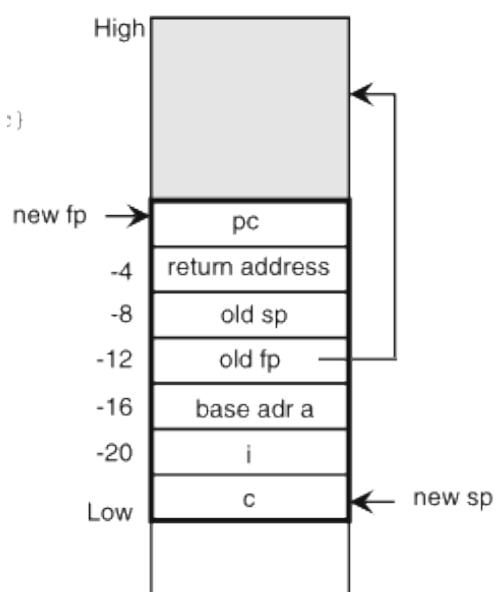


Fig. 7.9 (right side) corrige