Fast Packed String Matching for Short Patterns

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Abstract

Searching for all occurrences of a pattern in a text is a fundamental problem in computer science with applications in many other fields, like natural language processing, information retrieval and computational biology. In the last two decades a general trend has appeared trying to exploit the power of the word RAM model to speed-up the performances of classical string matching algorithms. In this model an algorithm operates on words of length \( w \), grouping blocks of characters, and arithmetic and logic operations on the words take one unit of time.

In this paper we use specialized word-size packed string matching instructions, based on the Intel streaming SIMD extensions (SSE) technology, to design very fast string matching algorithms in the case of short patterns. From our experimental results it turns out that, despite their quadratic worst case time complexity, the new presented algorithms become the clear winners on the average for short patterns, when compared against the most effective algorithms known in literature.

1 Introduction

Given a text \( t \) of length \( n \) and a pattern \( p \) of length \( m \) over some alphabet \( \Sigma \) of size \( \sigma \), the exact string matching problem consists in finding all occurrences of the pattern \( p \) in \( t \). This problem has been extensively studied in computer science because of its direct application to many areas. Moreover string matching algorithms are basic components in many software applications and play an important role in theoretical computer science by providing challenging problems.

In a computational model where the matching algorithm is restricted to read all the characters of the text one by one the optimal complexity is \( O(n) \), and was achieved the first time by the well known Knuth-Morris-Pratt algorithm [22] (KMP). However in many practical cases it is possible to avoid reading all the characters of the text achieving sub-linear performances on average. The optimal average \( O(\frac{n \log_w m}{m}) \) time complexity [30] was reached for the first time by the Backward-DAWG-

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Matching algorithm [8] (BDM). However, all algorithms with a sub-linear average behavior may have to read all the text characters in the worst case. It is interesting to note that many of those algorithms have an even worse \( O(nm) \)-time complexity in the worst-case [7].

In the last two decades a lot of work has been made in order to exploit the power of the word RAM model of computation to speed-up classical string matching algorithms. In this model, the computer operates on words of length \( w \), thus blocks of characters are read and processed at once. This means that usual arithmetic and logic operations on the words all take one unit of time.

Most of the solutions which exploit the word RAM model are based on the bit-parallelism technique or on the packed string matching technique.

The bit-parallelism technique [1] takes advantage of the intrinsic parallelism of the bit operations inside a computer word, allowing to cut down the number of operations that an algorithm performs by a factor up to \( w \). Bit-parallelism is particularly suitable for the efficient simulation of nondeterministic automaton. The first algorithm based on it, named Shift-Or [1] (SO), simulates efficiently the nondeterministic version of the KMP automaton and runs in \( O(n \frac{w}{w}) \), which is still considered among the best practical algorithms in the case of very short patterns and small alphabets [17, 15]. Later a very fast BDM-like algorithm (BNDM), based on the bit-parallel simulation of the nondeterministic suffix automaton, was presented in [26]. Some variants of the BNDM algorithm [12, 13, 9, 27] are among the most practical efficient solutions in literature (see [17, 15]). However, the bit-parallel encoding requires one bit per pattern symbol, for a total of \( \lceil \frac{n}{w} \rceil \) computer words. Thus, as long as a pattern fits in a computer word, bit-parallel algorithms are extremely fast, otherwise their performances degrades considerably as \( \lceil \frac{n}{w} \rceil \) grows. Though there are a few techniques to maintain good performance in the case of long patterns [24, 10, 5, 6], such limitation is intrinsic.

In the packed string matching technique multiple characters are packed into one larger word, so that the characters can be compared in bulk rather than individually. In this context, if the characters of a string are drawn from an alphabet of size \( \sigma \), then \( \lceil \frac{w}{\log \sigma} \rceil \)
and the specialized packed string instructions, the alternative solution with \( O(\frac{n}{\log \sigma} + m + occ) \)-time complexity, where \( \varepsilon > 0 \) denotes an arbitrary small constant, and \( occ \) denotes the number of occurrences of \( p \) in \( t \). Bille [4] presented an alternative solution with \( O(\frac{n}{\log \sigma} + m + occ) \)-time and \( O(n^\epsilon + m) \)-space complexities by an efficient segmentation and coding of the KMP automaton. Recently Belazzougui [2] proposed a packed string matching algorithm which works in \( O(\frac{n}{m} + \frac{n}{\alpha} + m + occ) \) time and \( O(m) \) space, reaching the optimal \( O(\frac{n}{\alpha} + occ) \)-time bound for \( \alpha < m \leq \frac{n}{\sigma} \). However, none of these results is of any practical interest.

The first algorithm that achieves good practical and theoretical results was very recently proposed by Fredriksson [18]. He presented a new practical and efficient algorithm for the exact packed string matching problem that turns out to be faster than the best algorithms known in literature in the case of short patterns. The algorithm, named Exact Packed String Matching (EPSM), is based on three different search procedures used for, respectively, very short patterns \( (0 < m < \frac{\sigma}{\varepsilon}) \), short patterns \( (\frac{\varepsilon}{\sigma} < m < \alpha) \) and medium length patterns \( (m \geq \alpha) \). They use specialized packed string instructions with a low latency and throughput, if compared with those used in [3]. All search procedures have an \( O(nm) \) worst case time complexity. However, they have very good performances on average.

2 Notions and Terminology

Throughout the paper we will make use of the following notations and terminology. A string \( p \) of length \( m > 0 \) is represented as a finite array \( p[0 \ldots m-1] \) of characters from a finite alphabet \( \Sigma \) of size \( \sigma \). Thus \( p[i] \) will denote the \((i + 1)\)-st character of \( p \), for \( 0 \leq i < m \), and \( p[i \ldots j] \) will denote the factor (or substring) of \( p \) contained between the \((i + 1)\)-st and the \((j + 1)\)-st characters of \( p \), for \( 0 \leq i < j < m \). In some cases we will denote by \( p_i \) the \((i + 1)\)-st character of \( p \), so that \( p_i = p[i] \) and \( p = p_0 p_1 \ldots p_{m-1} \).

We indicate with symbol \( w \) the number of bits in a computer word and with symbol \( \gamma = \lceil \log \sigma \rceil \) the number of bits used for encoding a single character of the alphabet \( \Sigma \). The number of characters of the alphabet that fit in a single word is shown by \( \alpha = \lfloor w/\gamma \rfloor \). Without lose in generality we will assume along the paper that \( \gamma \) divides \( w \) and that \( \alpha \) is an even value.

In chunks of \( \alpha \) characters, the string \( p \) is represented by an array \( P[0 \ldots k - 1] \) of length \( k = (m - 1)/\alpha + 1 \). In particular we denote \( P = P_0 P_1 P_2 \ldots P_{k-1} \), where \( P_i = p_{\alpha i} p_{\alpha i + 1} p_{\alpha i + 2} \ldots p_{\alpha i + \alpha - 1} \), for \( 0 \leq i < k \). The last block \( P_{k-1} \) is not complete if \( m \mod \alpha \neq 0 \). In that case, the rightmost remaining characters of the block case are set to zero.

Although different values of \( \alpha \) and \( \gamma \) are possible, in most cases we assume that \( \alpha = 16 \) and \( \gamma = 8 \), which is the most common case when working with characters in ASCII code and in a word RAM model with 128-bit registers, which are almost all available in recent commodity processors supporting single instruction multiple data (SIMD) operations.

Finally, we recall the notation of some bitwise infix operators on computer words, namely the bitwise and \( "\&" \), the bitwise or \( "|" \) and the left shift \( "<<" \) operator (which shifts to the left its first argument by a number of bits equal to its second argument).
3 A New Packed String Matching Algorithm

In this section we present a new packed string matching algorithm, named Exact Packed String Matching algorithm (EPSM), which turns out to be efficient in the case of short patterns. EPSM is based on three different auxiliary algorithms, which we name EPSMa, EPSMb and EPSMc, respectively.

The first two auxiliary algorithms are designed to search for patterns of length, at most, $\alpha/2$. When the length of the pattern is longer than $\alpha/2$ the algorithms adopt a filter mechanism: they first search for a substring of the pattern of length $\alpha/2$ and, when a candidate occurrence has been found, a naive check follows. The third algorithm adopts a filtering based solution.

All three algorithms run in $O(nm)$ worst case time complexity and use, respectively, $O(\min\{m,\alpha\})$, $O(1)$ and $O(2^k)$ additional space, where $k$ is a constant parameter. However, when $m \leq \alpha/2$ the EPSMa and EPSMb algorithms reach, respectively, an $O(m + \alpha + \text{occ})$ and $O(\alpha + \text{occ})$ time complexity. The first search procedure is designed to be extremely fast in the case of very short patterns, i.e. when $m \leq \frac{\alpha}{2}$, the second algorithm turns out to be a good choice when $\frac{\alpha}{2} \leq m < \alpha$, while the third algorithm turns out to be effective when $m \geq \alpha$. In practical cases we tuned the EPSM algorithm in order to run EPSMa when $0 < m < 4$, EPSMb when $4 \leq m < 16$, and to run EPSMc in all other cases.

In what follows, we first describe in Section 3.1 the computational model we use for the description of our solutions. Then we independently present the three auxiliary algorithms EPSMa, in Section 3.2, EPSMb, in Section 3.3, and EPSMc in Section 3.4.

3.1 The Model

In the design of our algorithms we use specialized word-size packed string matching instructions, based on the Intel streaming SIMD extensions (SSE) technology. SIMD instructions exist in many recent microprocessors supporting parallel execution of some operations on multiple data simultaneously via a set of special instructions working on limited number of special registers. Although the usage of SIMD is explored deeply in multimedia processing, implementation of encryption/decryption algorithms, and on some scientific calculations, it has not been much addressed in pattern matching.

In our model of computation we suppose that $w$ is the number of bits in a word and $\sigma$ is the size of the alphabet. We indicate with the symbol $\alpha = \frac{w}{\log\sigma}$ the number of characters which fit in a single computer word.

In most practical applications we have $\sigma = 256$ (ASCII code). Moreover SSE specialized instructions allow to work on 128-bit registers, thus reading and processing blocks of sixteen 8-bit characters in a single time unit (thus $\alpha = 16$).

In the design of our algorithms we make use of the following specialized word-size packed instructions. For each instruction we describe how it could be emulated by using SSE specialized intrinsics.

\textbf{wscmp}(a, b) \ (\textit{word-size compare instruction})

Compares two $w$-bit words, handled as a block of $\alpha$ characters. In particular if $a = a_0a_1 \ldots a_{\alpha-1}$ and $b = b_0b_1 \ldots b_{\alpha-1}$ are the two $w$-bit integer parameters, \textbf{wscmp}(a, b) returns an $\alpha$-bit value $r = r_0r_1 \ldots r_{\alpha-1}$, where $r_i = 1$ if and only if $a_i = b_i$, and $r_i = 0$ otherwise. Fig.1 shows an example of the application of \textbf{wscmp}(a, b), assuming $w = 48$, $\gamma = 4$ and $\alpha = 12$.

The \textbf{wscmp} specialized instruction can be emulated in constant time by using the following sequence of specialized SIMD instructions

\begin{verbatim}
    h ← _mm_cmpeq_epi8(a, b)
    r ← _mm_movemask_epi8(h)
\end{verbatim}

Specifically the \textit{\_mm_cmpeq_epi8} instruction compares two 128-bit words, handled as a block of sixteen 8-bit values, and returns a 128-bit value $h = h_0h_1 \ldots h_{15}$, where $h_i = 1^8$ if and only if $a_i = b_i$, and $h_i = 0^8$ otherwise. It has a 0.5-cycle throughput and a 1-cycle latency. The \textbf{\_mm_movemask_epi8} instruction gets a 128-bit parameter $h$, handled as sixteen 8-bit integers, and creates a 16-bit mask from the most significant bits of the 16 integers in $h$, and zero extends the upper bits.

\textbf{wsmatch}(a, b) \ (\textit{word-size matching instruction})

Reports all occurrences of a short string $b$ in a $w$-bit parameter $a$, handled as a string of $\alpha$ characters. The parameter $b$ is a string of length $k \leq \alpha$.

Specifically, if $a = a_0a_1 \ldots a_{\alpha-1}$, and $b = b_0b_1 \ldots b_{k-1}$, then the \textbf{wsmatch}(a, b) instruction returns an $\alpha$-bit integer value, $r = r_0r_1 \ldots r_{\alpha-1}$, where $r_i = 1$ if and only if $a_{i+j} = b_j$ for $j = 0, \ldots, k - 1$, i.e. an occurrence of $b$ in $a$ begins at position $i$. Notice that $r_i = 0$ for $\alpha - k < i < \alpha$, since no occurrence of $b$ in $a$ could begin at a position greater than $\alpha - k$. Fig.2 shows an example of the application of \textbf{wsmatch}(a, b), assuming $w = 48$, $\gamma = 4$, $\alpha = 12$ and $k = 3$.

The \textbf{wsmatch}(a, b) instruction can be emulated in constant time by using the following sequence of SIMD specialized instructions

\begin{verbatim}
    h ← _mm_mpsadbw_epu8(a, b)
    ℓ ← _mm_cmpeq_epi8(h, z)
    r ← _mm_movemask_epi8(ℓ)
\end{verbatim}
where \( z \) is a 128-bit register with all bits set to 0, i.e. \( z = 0^{128} \).

Specifically the \_mm_mpsadbw\_epu8\((a, b)\) instruction gets two 128-bit words, handled as a block of sixteen 8-bit values, and returns a 128-bit value \( r = r_0r_1 \ldots r_7 \) (handled as a block of eight 16-bit values), where \( r_i \) is computed as \( r_i = \sum_{j=0}^{i} |a_{i+j} - b_j| \) for \( i = 0 \ldots 7 \).

Thus we have that \( r_i = 0^{16} \) if and only if \( a_{i+j} = b_j \) for \( j = 0 \ldots 3 \), i.e. an occurrence of the prefix of \( b \) with length 4 begins in \( a \) at position \( i \). The \_mm_mpsadbw\_epu8 instruction has 1-cycle throughput and a 4-cycle latency. The \_mm_cmpeq\_epi8 and \_mm_movemask\_epi8 instructions have been described above.

wsblend\((a, b)\) (word-size blend instruction)
blends two \( w \)-bit parameters, handled as two blocks of \( \alpha \) characters. Specifically if \( a = a_0a_1 \ldots a_{\alpha-1} \) and \( b = b_0b_1 \ldots b_{\alpha-1} \), the instruction returns a \( w \)-bit integer \( r = r_0r_1 \ldots r_{\alpha-1} \), where \( r_i = a_{\alpha+i/2} \) if \( 0 \leq i < \alpha/2 \), and \( r_i = b_{\alpha-i/2} \) if \( \alpha/2 \leq i < \alpha \), i.e. \( r = a_0a_2a_4 \ldots a_{\alpha-2}b_1b_3b_5 \ldots b_{\alpha-1} \). Fig.3 shows an example of the application of wsblend\((a, b)\), assuming \( w = 48 \), \( \gamma = 4 \) and \( \alpha = 12 \).

The wsblend\((a, b)\) instruction can be emulated in constant time by using the following sequence of SIMD specialized instructions

\[
\begin{align*}
    h & \leftarrow _{\text{mm\_blend\_epi16}}(a, b, c) \\
    \text{SHUFFLE} & \leftarrow _{\text{MM\_SHUFFLE}}(1, 0, 3, 2) \\
    r & \leftarrow _{\text{mm\_shuffle\_epi32}}(h, \text{SHUFFLE})
\end{align*}
\]

Such instruction blends two 128-bit integers, \( a = a_0a_1 \ldots a_7 \) and \( b = b_0b_1 \ldots b_7 \), handled as packed 16-bit integers, according to a third parameter \( c \). In particular it returns a 128-bit integer \( r = r_0r_1 \ldots r_7 \) where \( r_i = a_i \) if \( c_i = 0 \), and \( r_i = b_i \) otherwise. If we set \( c = 0^{64}1^{64} \) we get \( r = a_0a_1a_2a_3b_4b_5b_6b_7 \). The \_mm\_blend\_epi16 instruction has 0.5-cycle throughput and a 1-cycle latency. The \_mm\_shuffle\_epi32 instruction shuffles a \( w \)-bit parameter, \( a = a_0a_1a_2a_3 \), handled as four 32-bit values, according to the order of the \_MM\_SHUFFLE macro. In this case we get \( r = a_2a_3a_0a_1 \). The \_mm\_shuffle\_epi32 instruction has 1-cycle throughput and a 1-cycle latency.

wscrc\((a)\) (word-size cyclic redundancy check)
computes the 32-bit cyclic redundancy checksum (CRC) signature for a \( w \)-bit parameter. It is an error-detecting code commonly used in digital networks and storage devices to detect accidental changes to raw data and can also be used as a hash function.

The wscrc\((a)\) instruction can be emulated in constant time by using the \_mm\_crc32\_ucrc\((a)\) SIMD specialized instructions, which computes the 32 bit cyclic redundancy checksum of a 64-bit block according to a polynomial. Such instruction has a 1-cycle throughput and a 3-cycle latency, thus provides a robust and fast way of computing hash values.
Additional specialized instructions
In addition to the above listed instructions, given an α-bit register $r$, in our description we make use of the symbol \{α\} to indicate the set of bits in $r$ whose value is set. More formally, given an α-bit register $r = r_0r_1r_2 \ldots r_{α-1}$, we have \{α\} = \{ i \mid 0 ≤ i < α \text{ and } r_i = 1 \}. Moreover, given a value $s ∈ \mathbb{N}$, we use for simplicity the expression $s + \{α\}$ to indicate the set of values \{ s + i \mid i ∈ \{α\}\}.

The cardinality of the set \{α\} can be computed in constant time by using the SIMD specialized instructions _mm_popcnt_u32(r) which calculates the number of bits of the parameter $r$ that are set to 1. Such instruction has 1-cycle throughput and a 3-cycle latency.

Differently the list of values in \{α\} can be efficiently listed in $O(α)$-time and $O(1)$-space, or using a tabulation approach, in $O(|\{α\}|)$-time and $O(2^α)$-space. In the latter case we need a $O(α2^α)$-time preprocessing phase in order to address the $2^α$ possible registers.

We are now ready to describe the three auxiliary algorithms used in the EPSM algorithm. The pseudocode of the three algorithms is shown in Fig. 4.

3.2 EPSMα: Searching for Very Short Patterns
The EPSMα algorithm is designed to be extremely fast in the case of very short patterns and although it could be adapted to work for longer patterns its performances degrades as the length of the patterns increase.

The preprocessing of the algorithm (lines 1-4) is computed on the prefix of the pattern of length $m' = \min\{m, \frac{α}{2}\}$. If $m' = m$ the whole pattern is preprocessed and searched, otherwise the algorithm works as a filter, searching for all occurrences of the prefix with length $m'$ and, after an occurrence has been found, naively checking the whole occurrence of the pattern.

Specifically the preprocessing phase consists in constructing an array $B$ of $m'$ different strings of length $α$. Each string of the array exactly fits in a word of $w$ bits. The $i$-th string in the array $B$ consists of $α$ copies of the character $p_i$. More formally the string $B[i]$, for $0 ≤ i < m'$, is defined as $B[i] = (p_i)^α$. For instance, if $p = ab$ is a pattern of length $m = 2$, $γ = 8$ and $w = 128$, then $B$ consists of two strings of length $α = 16$, defined as $B[0] = a^{16}$ and $B[1] = b^{16}$. The preprocessing phase of the algorithm requires $O(\min\{m, \frac{α}{2}\})$-time and $O(\min\{m, \frac{α}{2}\})$-space.

The searching phase of the algorithm (lines 5-14) processes the text $t$ in chunks of $α$ characters. Let $N = \frac{α}{2}$ and let $T = T_0T_1 \ldots T_N$ be the string $t$ represented in chunks of characters. Each block of the text, $T_i$, is compared with the strings in the array $B$ using the instruction _wscmp_.

Let $s_j = b_0b_1 \ldots b_{α-1}$ be the $α$-bit register returned by the instruction _wscmp_(Ti, B[j]), for $0 ≤ j < m'$. It can be easily proved that $b_k = 1$ if and only if the $k$-th character of the block $T_i$ is equal to $p_j$, i.e. if and only if $T_i[k] = p_j$ (remember that $B[j] = (p_j)^α$). Finally let $r = r_0r_1 \ldots r_{α-1}$ be the $α$-bit register defined as $r = s_0 \& (s_1 ≪ 1) \& (s_2 ≪ 2) \& \cdots \& (s_{m'-1} ≪ (m'-1))$.

It is easy to prove that $p[0 \ldots m'-1]$ has an occurrence beginning at position $j$ of $T_i$ if and only if $r_j = 1$. In fact $r_j = 1$ only if $s_k[j+k] = 1$, for $k = 0 \ldots m'-1$, which implies that $T_i[j+k] = p_k$, for $k = 0 \ldots m'-1$. Then, if $m = m'$ the algorithm reports the occurrences of the pattern at positions $ια + \{α\}$, if any. Otherwise we know that occurrences of the prefix of the pattern with length $α/2$ begin at positions $ια + \{α\}$. Thus the algorithm checks the occurrences beginning at those positions.

If we maintain, for each value $r$, with $0 ≤ r < 2^α$, a list of the values in the set $\{r\}$, the naive check of the occurrences can be done in $O(|\{r\}|)$-time. When $m = m'$ the occurrences can be reported in $O(|\{r\}|)$-time. Finally, observe that the $m'-1$ possible occurrences crossing the blocks $T_i$ and $T_{i+1}$ are naively checked by the algorithm (lines 13-14).

The overall time complexity of the EPSMα algorithm is $O(αm)$, because in the worst case a naive check is required for each position of the text. However, when $m ≤ \frac{α}{2}$ the EPSMα algorithm achieves a $O(n + occ)$ time complexity, where occ is the number of occurrences of the pattern $p$ in the text $t$. 

![Figure 3: An example of the application of wsmatch(a, b), assuming w = 48, γ = 4 and α = 12](image-url)
Figure 4: The EPSM\textsubscript{a} (on the top), the EPSM\textsubscript{b} (in the middle) and the EPSM\textsubscript{c} (on the bottom) auxiliary algorithms.

3.3 EPSM\textsubscript{b}: Searching for Short Patterns\ The EPSM\textsubscript{b} searches for the whole pattern when its length is less or equal to $\alpha/2$ and works as a filter algorithm for longer patterns. However, it is based on a more efficient filtering technique and turns out to be faster in the second case.

Let $m'$ be the minimum between $\alpha/2$ and $m$. Moreover let $p'$ be the prefix of $p$ of length $m'$. The searching phase of the algorithm (lines 3-14) processes the text $t$ in chunks of $\alpha$ characters. Let $N = \frac{2}{\alpha} - 1$ and let $T = T_0T_1 \ldots T_N$ be the string $t$ represented in chunks of characters. Each block of the text, $T_i$, is searched one by one for occurrences of the string $p'$ using the instruction \texttt{wsmatch}.

Specifically, let $r = r_0r_1 \ldots r_{\alpha-1}$ be the $\alpha$-bit register returned by the instruction \texttt{wsmatch}($T_i, p'$), for $0 \leq j < m'$. We have that $r_j = 1$ if and only if an occurrence of $p'$ begins at positions $j$ of the block $T_i$, for $0 \leq j < \alpha/2$. Then, if $m' = m$ (and hence $p = p'$) the algorithm simply returns positions $i\alpha + j$, such that $r_j = 1$. Otherwise, if $m' < m$, the algorithm naively checks for the whole occurrences of the pattern starting at positions $i\alpha + j$, such that $r_j = 1$.

Notice that generally packed string matching instructions allow to read only blocks $T_i$ of $\alpha$ characters (128 bits in the case of SSE instructions), where $T_i = t[i\alpha \ldots (i+1)\alpha - 1]$. Occurrences of the pattern beginning in the second half of the block $T_i$ are checked separately. In particular a new block, $S$, obtained by applying the instruction \texttt{wsblend}(\texttt{wsmatch}($T_i, p'$), is processed in a similar way as block $T_i$. In this case we report all occurrences of the pattern beginning at positions $i\alpha + \alpha/2 + j$, with $0 \leq j < \alpha/2$. One may argue that why blending is used instead of simply shifting the window. The reason is the SSE instructions used in this context require the operands to be 16-byte aligned in memory, where the performance degrades significantly otherwise. Thus, blending is more advantageous.

The resulting algorithm has an $O(nm)$ worst case time complexity and require $O(1)$ additional space. When $m \leq \alpha/2$ the algorithm reaches the optimal $O(n/\alpha + occ)$ worst case time complexity.

3.4 EPSM\textsubscript{c}: Searching for Medium Length Patterns\ The EPSM\textsubscript{c} algorithm is designed to be faster for medium length patterns. It is based on a simple filtering method and uses a hash function for computing fingerprint values on blocks of $\alpha$ characters in a similar way as in Rabin-Karp algorithm [21]. The fingerprint values are computed by using a hash function $h : \Sigma^\alpha \to \{0, 1, \ldots, 2^k - 1\}$, for a constant parameter $k$ (in practice we chose $k = 11$).

The function $h$ is computed in a very fast way by using the \texttt{wscrc} specialized instruction, and in particular $h(a) = \text{wscrc}(a) \& 0^\alpha - k1^k$, for each $A \in \Sigma^\alpha$.

During the preprocessing phase (lines 1-6) a fingerprint value of $k$ bits is computed for all substrings of
the pattern of length $\alpha$. Then a table $L$ of size $2^k$ is computed in order to store starting positions of all sub-
strings of the pattern, indexed by their fingerprint val-
ues. In particular we have $L[v] = \{i \mid h(p[i..i+\alpha-1]) = v\}$, for all $0 \leq v < 2^k$.

The preprocessing phase of the EPSM algorithm takes $O(nm+2^k)$-time.

Let $N = \frac{2^k}{\alpha} - 1$ and let $T = T_0T_1 \ldots T_N$ be the string $t$ represented in chunks of characters. During
the searching phase (lines 7-13) the EPSM algorithm inspects
the blocks of the text in steps of $(\lfloor m/\alpha \rfloor - 1) \cdot \alpha$ positions. For each inspected block $T_i$ the fingerprint value $h(T_i)$ is computed and all positions in the set
$\{\alpha i - j \mid j \in L[h(T_i)]\}$ are naively checked. The EPSM algorithm has a $O(nm)$ worst case time complexity but turns out to be very effective in practical cases.

4 Experimental Results

In this section we present experimental results in order
to compare the performances of our newly presented
algorithms against the best solutions known in litera-
ture in the case of short patterns. We consider all the
fastest algorithms in the case of short patterns as listed
in a recent experimental evaluation by Faro and Lecroq
[17, 15]. In particular we compared EPSM with the
following algorithms:

- the Hash algorithm using groups of $q$ characters [25] (HASH$q$);
- the Extended Backward Oracle Matching algorithm
[12, 13] (EBOM);
- the TVSBS algorithm [29] (TVSBS);
- the Shift-Or algorithm [1] (SO);
- the Shift-Or algorithm with $q$-grams [9] (UFNDM$q$);
- the Fast-Average-Optimal-Shift-Or algorithm [19] (FAOSO$q$);
- the Backward DAWG Matching algorithm using
$q$-grams [9] (BNDM$q$);
- the Simplified BNDM$q$ algorithm [9] (SBNDM$q$);
- the Forward BNDM$q$ algorithm [12, 13, 27] (FBNDM$q$);
- the Crochemore-Perrin algorithm using SSE instructions [3] (SSECP);

We remember that the EPSM algorithm consists of the
EPSM algorithm, when $m < 4$, of the EPSMb algorithm when
$4 \leq m \leq 16$, and of the EPSMc algorithm when $m > 16$.

In the case of algorithms making use of $q$ grams, the value of $q$ ranges in the set $\{2, 4, 6\}$. All algorithms
have been implemented in the C programming language
and have been tested using the Smart tool [16] for exact
string matching. The experiments were executed locally
on a machine running Ubuntu 11.10 (oneiric) with Intel
i7-2600 processor with 16GB memory. Algorithms have
been compared in terms of running times, including
any preprocessing time. For the evaluation we used
a genome sequence, a protein sequence and a natural
language text (English language), all sequences of 4MB.
The sequences are provided by the Smart research tool.
For each input file, we have searched sets of 1000
patterns of fixed length $m$ randomly extracted from the
text, for $m$ ranging from 2 to 32 (short patterns). Then,
the mean of the running times has been reported.

Table 1, Table 2 and Table 3 show the experimental
results obtained for a genome sequence, a protein se-
quence and a natural language text, respectively.

In the case of algorithms using $q$-grams we have
reported only the best result obtained by its variants.
The values of $q$ which obtained the best running times
are reported as apices. Running times are expressed in
hundredths of seconds, best results have been boldfaced
and underlined.

From experimental results it turns out that the
EPSM algorithm has mostly the best performances for
short patterns. When searching on a genome sequence it
is second only to the BNDM$q$ algorithm for $12 \leq m \leq 14$
and to the SSECP algorithm when $m = 6$. Observe
however that the EPSM algorithm is (up to 2 times)
faster than the SSECP algorithm in most cases.

When searching on a natural language text the
EPSM algorithm obtains in most cases the best results,
and is second to BNDM based algorithms only for
$20 \leq m \leq 22$.

For increasing lengths of the pattern the perfor-
mances of the EPSM algorithm remain stable, under-
lining a linear trend on average. However, the perfor-
mances of other algorithms based on shift heuristics,
slightly increases. This is more evident when searching
on a protein sequence, where the algorithms based on
bit-parallelism and $q$ grams turn out to be the faster
solutions for longer patterns. However, in this latter
cases the EPSM algorithm is always very close the best
solutions.

It is interesting to observe that the EPSM algorithm
is faster than the SSECP algorithm in almost all cases,
and the gap is more evident in the case of longer
patterns. In fact, despite its optimal worst case time
complexity, the SSECP algorithm shows an increasing
trend on average, while the EPSM algorithm shows a
linear behavior.

5 Conclusions

We presented a new packed exact string matching
algorithm based on the Intel streaming SIMD extensions
technology. The presented algorithm, named EPSM, is
short patterns. From our experimental results it turns out that the EPSM algorithm is in general the best based on three auxiliary algorithms which are used when $0 < m < 4$, $m = 4$, and $m \geq 16$, respectively. Despite the $O(nm)$-worst case time complexity the resulting algorithm turns out to be very fast in the case of very short patterns. From our experimental results it turns out that the EPSM algorithm is in general the best solutions when $m \leq 32$. It could be interesting to investigate the possibility to improve the performances of packed string matching algorithms by introducing shift heuristics.
References


